

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Moore (et al.)**
Serial No.: **10/801,942**
Filed: **March 16, 2004**

Attorney Docket: **0057-011**
Examiner: **Partridge, William B.**
Group Art Unit: **2183**

Title: **Computer Processor Array With Dedicated Interconnections (as amended)**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.: **40972**

TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

Enclosed for filing in the above-referenced application are the following:

1. Request for Continued Examination (RCE);
2. Information Disclosure Statement (3 pages);
3. Forms PTO/SB/08a and PTO/SB/08b (2 pages); and
4. 13 non-United States patent cited references.

Respectfully Submitted,

February 10, 2011
Date: _____

/Larry E. Henneman, Jr./

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CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being filed with the United States Patent and Trademark Office on the date shown below via EFS-Web.

February 10, 2011
Date: _____

/Larry E. Henneman, Jr./

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